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-- *****
-- **** STUDENT: 64000225
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2*n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64190088
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64200100
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64200112
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****

LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2*n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64200163
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64200238
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64200288
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64200296
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2*n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64200385
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64210113
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64210290
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64210382
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64210384
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

```

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-- *****
-- **** STUDENT: 64210386
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

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-- *****
-- **** STUDENT: 64210445
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

```

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-- *****
-- **** STUDENT: 64210455
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

```

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-- *****
-- **** STUDENT: 64210457
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

```

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-- *****
-- **** STUDENT: 64240430
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

```

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-- *****
-- **** PREDLOGA VAJE
-- *****
-- KOMENTARJI K OCENI NALOGE
-- Matej Možek: Ni pripomb
-- *****
LIBRARY ieee;
USE ieee.std_logic_1164.all;
use ieee.numeric_std.all;

ENTITY muxnto1 IS
    generic( n_addr: natural := 2 );
    PORT ( s      : in  std_logic_vector( n_addr - 1 downto 0 );
          w      : in  std_logic_vector( 2**n_addr - 1 downto 0 );
          f      : OUT STD_LOGIC
          );
END muxnto1;

ARCHITECTURE ideal OF muxnto1 IS
BEGIN
    f      <= w( to_integer( unsigned( s ) ) );
END ideal;

```

