

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

64200100

[illegible]

[illegible]

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0



ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 15	IR: 57831	CMD: JUMP 62336	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

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ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0		

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0



IDEAL

ROM ADDR	ROM IR	ASM op	R7	R6	R5	R4	R3	R2	R1	R0	M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	M3	M2	M1	M0
addr: 0	IR: 40593	CMD: R(2)<=1									0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 1	IR: 15831	CMD: R(7)<=M(R(2))	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 2	IR: 7751	CMD: R(1)<=R(7)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 3	IR: 33018	CMD: R(3)<= R(7)+2	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 4	IR: 33359	CMD: R(1)<= R(1)-7	2	0	0	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 5	IR: 49676	CMD: IF R(1) < 0; (PC= PC + (4))	2	0	0	0	4	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 6	IR: 11	CMD: R(0)<=R(1) PLUS R(3)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 7	IR: 24088	CMD: M(R(3))<=R(0)	2	0	0	0	4	1	-5	0	0	0	0	0	0	0	0	0	0	0	0	0	-14	6	2	0
addr: 8	IR: 8007	CMD: R(5)<=R(7)	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 9	IR: 56773	CMD: IF R(0) = 0; (PC= PC + (-3))	2	0	0	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	0	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 14	IR: 50164	CMD: IF R(6) < 0; (PC= PC + (-4))	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 10	IR: 15751	CMD: R(6)<=M(R(0))	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 11	IR: 6542	CMD: R(6)<=R(1) XOR R(6)	2	-6	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 12	IR: 7942	CMD: R(4)<=R(6)	2	0	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0
addr: 13	IR: 1968	CMD: R(6)<=R(6) MINUS 1	2	-5	2	-5	4	1	-5	-1	0	0	0	0	0	0	0	0	0	0	0	-1	-14	6	2	0