

>cat ->HDL Compiler	>cat ->HDL Compiler
Release 14.7 - xst P.20131013 (n654)	Release 14.7 - xst P.20131013 (n654)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.	Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
-> Parameter TMPDIR set to /xst/prjcnv/tmp	-> Parameter TMPDIR set to /xst/prjcnv/tmp
Total REAL time to Xst completion: 1.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.24 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.
Package <cordic_pka> compiled.	Package <cordic_pka> compiled.
Package body <cordic_pka> compiled.	Package body <cordic_pka> compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.
Entity <cia_add_n_bit> compiled.	Entity <cia_add_n_bit> compiled.
Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Entity <CORDIC_ROM> compiled.	Entity <addsub> compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Entity <addsub> compiled.	Entity <CORDIC_ROM> compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Entity <cordic> compiled.	Entity <cordic> compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Total REAL time to Xst completion: 1.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.95 secs	Total CPU time to Xst completion: 0.46 secs
->	->
Total memory usage is 4477188 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -prj CORDIC_logging.tb -top CORDIC tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -prj CORDIC_logging.tb -top CORDIC tb
(Isim P.20131013 (signature 0x708090))	(Isim P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pka.vhd" into library work	Parsing VHDL file "cordic_pka.vhd" into library work
Parsing VHDL file "cia_add_n_bit.vhd" into library work	Parsing VHDL file "cia_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

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Total CPU time to Xst completion: 0.25 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pkv.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pkv.vhd" in Library work.
Package <cordic_pkv> compiled.	Package <cordic_pkv> compiled.
Package body <cordic_pkv> compiled.	Package body <cordic_pkv> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cda_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cda_add_n_bit.vhd" in Library work.
Entity <cda_add_n_bit> compiled.	Entity <cda_add_n_bit> compiled.
Entity <cda_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cda_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cda_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cda_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Entity <CORDIC_ROM> compiled.	Entity <addsub> compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Entity <addsub> compiled.	Entity <CORDIC_ROM> compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Entity <cordic> compiled.	Entity <cordic> compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Total REAL time to Xst completion: 1.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.46 secs	Total CPU time to Xst completion: 0.46 secs
->	->
Total memory usage is 4477156 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(Ism P.20131013 (signature 0x708090))	(Ism P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pkv.vhd" into library work	Parsing VHDL file "cordic_pkv.vhd" into library work
Parsing VHDL file "cda_add_n_bit.vhd" into library work	Parsing VHDL file "cda_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter_IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom_IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req_IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm_IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra_IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

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-> Parameter TMPDIR set to /xst/procdir/tmp	-> Parameter TMPDIR set to /xst/procdir/tmp
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.24 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.
Package <cordic_pkg> compiled.	Package <cordic_pkg> compiled.
Package body <cordic_pkg> compiled.	Package body <cordic_pkg> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.
Entity <cia_add_n_bit> compiled.	Entity <cia_add_n_bit> compiled.
Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <rtb>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Entity <addsub> compiled.	Entity <addsub> compiled.
Entity <addsub> (Architecture <rtb>) compiled.	Entity <addsub> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Entity <CORDIC_ROM> compiled.	Entity <CORDIC_ROM> compiled.
Entity <CORDIC_ROM> (Architecture <rtb>) compiled.	Entity <CORDIC_ROM> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Entity <barrel_shifter_sra> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Entity <cordic> compiled.	Entity <cordic> compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.42 secs	Total CPU time to Xst completion: 0.46 secs
->	->
Total memory usage is 4477095 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(Isim P.20131013 (signature 0x708090))	(Isim P.20131013 (signature 0x708090))
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Parsing VHDL file "rom IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

>cat -fH CORDIC.vst	>cat -fH CORDIC.vst
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-> Parameter TMPDIR set to /xst/prj/par/tmp	-> Parameter TMPDIR set to /xst/prj/par/tmp
Total REAL time to Xst completion: 1.58 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.24 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
HDL Compiler:	HDL Compiler:
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pkg.vhd" in Library work.
Entity <cia_add_n_bit> compiled.	Package <cordic_pkg> compiled.
Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.	Package body <cordic_pkg> compiled.
Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pkg.vhd" in Library work.	Entity <cia_add_n_bit> compiled.
Package <cordic_pkg> compiled.	Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.
Package body <cordic_pkg> compiled.	Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <up_counter> compiled.	Entity <data_req> compiled.
Entity <up_counter> (Architecture <rtl>) compiled.	Entity <data_req> (Architecture <rtl>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <CORDIC_ROM> compiled.	Entity <up_counter> compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtl>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Entity <data_req> (Architecture <rtl>) compiled.
Entity <data_req> compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Entity <data_req> (Architecture <rtl>) compiled.	Entity <addsub> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Entity <addsub> (Architecture <ndv>) compiled.
Entity <barrel_shifter_sra> compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <CORDIC_ROM> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Entity <addsub> compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Entity <addsub> (Architecture <ndv>) compiled.	Entity <barrel_shifter_sra> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Entity <CORDIC_FSM> compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Entity <CORDIC_FSM> (Architecture <rtl>) compiled.	Entity <CORDIC_FSM> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Entity <CORDIC_FSM> (Architecture <rtl>) compiled.
Entity <cordic> compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Entity <cordic> (Architecture <rtl>) compiled.	Entity <cordic> compiled.
Total REAL time to Xst completion: 2.09 secs	Entity <cordic> (Architecture <rtl>) compiled.
Total CPU time to Xst completion: 1.55 secs	Total REAL time to Xst completion: 0.00 secs
->	Total CPU time to Xst completion: 0.46 secs
->	->
Total memory usage is 4477172 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -prj CORDIC_logging.tb -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -prj CORDIC_logging.tb -top CORDIC.tb
(Sim P.20131013 (signature 0x708090))	(Sim P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pkg.vhd" into library work	Parsing VHDL file "cordic_pkg.vhd" into library work
Parsing VHDL file "cia_add_n_bit.vhd" into library work	Parsing VHDL file "cia_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter_IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom_IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req_IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm_IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra_IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

>cat ->HDL Compiler	>cat ->HDL Compiler
Release 14.7 - xst P.20131013 (n654)	Release 14.7 - xst P.20131013 (n654)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.	Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
-> Parameter TMPDIR set to /xst/bin/par/tmp	-> Parameter TMPDIR set to /xst/bin/par/tmp
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.23 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.
Package <cordic_pka> compiled.	Package <cordic_pka> compiled.
Package body <cordic_pka> compiled.	Package body <cordic_pka> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.
Entity <cia_add_n_bit> compiled.	Entity <cia_add_n_bit> compiled.
Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Entity <CORDIC_ROM> compiled.	Entity <addsub> compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Entity <addsub> compiled.	Entity <CORDIC_ROM> compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Entity <cordic> compiled.	Entity <cordic> compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Total REAL time to Xst completion: 1.09 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.42 secs	Total CPU time to Xst completion: 0.46 secs
->	->
Total memory usage is 4477144 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(Isim P.20131013 (signature 0x708090))	(Isim P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pka.vhd" into library work	Parsing VHDL file "cordic_pka.vhd" into library work
Parsing VHDL file "cia_add_n_bit.vhd" into library work	Parsing VHDL file "cia_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

>cat -fh CORDIC.vst	>cat -fh CORDIC.vst
Release 14.7 - xst P.20131013 (n654)	Release 14.7 - xst P.20131013 (n654)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.	Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
-> Parameter TMPDIR set to /xst/bin/par/tmp	-> Parameter TMPDIR set to /xst/bin/par/tmp
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.22 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
* HDL Compiler	* HDL Compiler
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.
Package <cordic_pka> compiled.	Package <cordic_pka> compiled.
Package body <cordic_pka> compiled.	Package body <cordic_pka> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.
Entity <cia_add_n_bit> compiled.	Entity <cia_add_n_bit> compiled.
Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Entity <addsub> compiled.
Entity <CORDIC_ROM> compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Entity <CORDIC_ROM> compiled.
Entity <addsub> compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Entity <cordic> compiled.
Entity <cordic> compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Total REAL time to Xst completion: 0.00 secs
Total REAL time to Xst completion: 0.00 secs	Total CPU time to Xst completion: 0.46 secs
Total CPU time to Xst completion: 0.65 secs	
->	->
Total memory usage is 4477132 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(Isim P.20131013 (signature 0x708090))	(Isim P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pka.vhd" into library work	Parsing VHDL file "cordic_pka.vhd" into library work
Parsing VHDL file "cia_add_n_bit.vhd" into library work	Parsing VHDL file "cia_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter_IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom_IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req_IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm_IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra_IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

>set -fH CORDIC.vst	>set -fH CORDIC.vst
Release 14.7 - xst P.20131013 (n654)	Release 14.7 - xst P.20131013 (n654)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.	Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
-> Parameter TMPDIR set to /xst/bin/rav.tmp	-> Parameter TMPDIR set to /xst/bin/rav.tmp
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.25 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
* HDL Compiler	* HDL Compiler
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.
Package <cordic_pkg> compiled.	Package <cordic_pkg> compiled.
Package body <cordic_pkg> compiled.	Package body <cordic_pkg> compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cda_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cda_add_n_bit.vhd" in Library work.
Entity <cda_add_n_bit> compiled.	Entity <cda_add_n_bit> compiled.
Entity <cda_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cda_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cda_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cda_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cda_req.vhd" in Library work.
Entity <CORDIC_FSM> compiled.	Entity <cda_req> compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Entity <cda_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_reg.vhd" in Library work.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <data_reg> compiled.	Entity <up_counter> compiled.
Entity <data_reg> (Architecture <rtb>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Entity <addsub> compiled.
Entity <barrel_shifter_sra> compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Entity <CORDIC_ROM> compiled.
Entity <CORDIC_ROM> compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Entity <barrel_shifter_sra> compiled.
Entity <addsub> compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Entity <CORDIC_FSM> compiled.
Entity <up_counter> compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Entity <cordic> compiled.
Entity <cordic> compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Total REAL time to Xst completion: 0.00 secs
Total REAL time to Xst completion: 0.00 secs	Total CPU time to Xst completion: 0.46 secs
Total CPU time to Xst completion: 0.43 secs	->
->	->
Total memory usage is 4477116 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(ISim P.20131013 (signature 0x7f08f090))	(ISim P.20131013 (signature 0x7f08f090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pka.vhd" into library work	Parsing VHDL file "cordic_pka.vhd" into library work
Parsing VHDL file "cda_add_n_bit.vhd" into library work	Parsing VHDL file "cda_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

>cat ->HDL Compiler	>cat ->HDL Compiler
Release 14.7 - xst P.20131013 (n654)	Release 14.7 - xst P.20131013 (n654)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.	Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
-> Parameter TMPDIR set to /xst/bin/par/tmp	-> Parameter TMPDIR set to /xst/bin/par/tmp
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.38 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.
Package <cordic_pka> compiled.	Package <cordic_pka> compiled.
Package body <cordic_pka> compiled.	Package body <cordic_pka> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.
Entity <cia_add_n_bit> compiled.	Entity <cia_add_n_bit> compiled.
Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Entity <CORDIC_ROM> compiled.	Entity <addsub> compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Entity <addsub> compiled.	Entity <CORDIC_ROM> compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Entity <cordic> compiled.	Entity <cordic> compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Total REAL time to Xst completion: 2.09 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 2.15 secs	Total CPU time to Xst completion: 0.46 secs
->	->
Total memory usage is 4477124 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(Isim P.20131013 (signature 0x708090))	(Isim P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pka.vhd" into library work	Parsing VHDL file "cordic_pka.vhd" into library work
Parsing VHDL file "cia_add_n_bit.vhd" into library work	Parsing VHDL file "cia_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter_IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom_IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req_IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm_IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra_IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std



>cat ->HDL Compiler	>cat ->HDL Compiler
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Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.	Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
-> Parameter TMPDIR set to /xst/procdir/tmp	-> Parameter TMPDIR set to /xst/procdir/tmp
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.22 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.
Package <cordic_pka> compiled.	Package <cordic_pka> compiled.
Package body <cordic_pka> compiled.	Package body <cordic_pka> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.
Entity <cia_add_n_bit> compiled.	Entity <cia_add_n_bit> compiled.
Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Entity <addsub> compiled.
Entity <CORDIC_ROM> compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Entity <CORDIC_ROM> compiled.
Entity <addsub> compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Entity <cordic> compiled.
Entity <cordic> compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Total REAL time to Xst completion: 0.00 secs
Total REAL time to Xst completion: 0.00 secs	Total CPU time to Xst completion: 0.46 secs
Total CPU time to Xst completion: 0.46 secs	->
->	->
Total memory usage is 4477144 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(Isim P.20131013 (signature 0x708090))	(Isim P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pka.vhd" into library work	Parsing VHDL file "cordic_pka.vhd" into library work
Parsing VHDL file "cia_add_n_bit.vhd" into library work	Parsing VHDL file "cia_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

>cat ->HDL Compiler	>cat ->HDL Compiler
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Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.	Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
-> Parameter TMPDIR set to /xst/bin/par/tmp	-> Parameter TMPDIR set to /xst/bin/par/tmp
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.23 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pka.vhd" in Library work.
Package <cordic_pka> compiled.	Package <cordic_pka> compiled.
Package body <cordic_pka> compiled.	Package body <cordic_pka> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cia_add_n_bit.vhd" in Library work.
Entity <cia_add_n_bit> compiled.	Entity <cia_add_n_bit> compiled.
Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cia_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cia_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Entity <CORDIC_ROM> compiled.	Entity <addsub> compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Entity <addsub> compiled.	Entity <CORDIC_ROM> compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Entity <cordic> compiled.	Entity <cordic> compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Total REAL time to Xst completion: 1.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.51 secs	Total CPU time to Xst completion: 0.46 secs
->	->
Total memory usage is 4477124 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(Isim P.20131013 (signature 0x708090))	(Isim P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pka.vhd" into library work	Parsing VHDL file "cordic_pka.vhd" into library work
Parsing VHDL file "cia_add_n_bit.vhd" into library work	Parsing VHDL file "cia_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter_IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom_IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req_IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm_IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra_IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

>cat ->HDL Compiler	>cat ->HDL Compiler
Release 14.7 - xst P.20131013 (n654)	Release 14.7 - xst P.20131013 (n654)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.	Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
-> Parameter TMPDIR set to /xst/bin/par/tmp	-> Parameter TMPDIR set to /xst/bin/par/tmp
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.23 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pkv.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pkv.vhd" in Library work.
Package <cordic_pkv> compiled.	Package <cordic_pkv> compiled.
Package body <cordic_pkv> compiled.	Package body <cordic_pkv> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cda_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cda_add_n_bit.vhd" in Library work.
Entity <cda_add_n_bit> compiled.	Entity <cda_add_n_bit> compiled.
Entity <cda_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cda_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cda_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cda_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Entity <CORDIC_ROM> compiled.	Entity <addsub> compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Entity <addsub> compiled.	Entity <CORDIC_ROM> compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Entity <cordic> compiled.	Entity <cordic> compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.41 secs	Total CPU time to Xst completion: 0.46 secs
->	->
Total memory usage is 4477144 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(Sim P.20131013 (signature 0x708090))	(Sim P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pkv.vhd" into library work	Parsing VHDL file "cordic_pkv.vhd" into library work
Parsing VHDL file "cda_add_n_bit.vhd" into library work	Parsing VHDL file "cda_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter_IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom_IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req_IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm_IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra_IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

>cat ->HDL Compiler	>cat ->HDL Compiler
Release 14.7 - xst P.20131013 (n654)	Release 14.7 - xst P.20131013 (n654)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.	Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
-> Parameter TMPDIR set to /xst/procdir/tmp	-> Parameter TMPDIR set to /xst/procdir/tmp
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.23 secs	Total CPU time to Xst completion: 0.22 secs
-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.	-> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence is considered.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pkv.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_pkv.vhd" in Library work.
Package <cordic_pkv> compiled.	Package <cordic_pkv> compiled.
Package body <cordic_pkv> compiled.	Package body <cordic_pkv> compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cda_add_n_bit.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cda_add_n_bit.vhd" in Library work.
Entity <cda_add_n_bit> compiled.	Entity <cda_add_n_bit> compiled.
Entity <cda_add_n_bit> (Architecture <for_loop_arch>) compiled.	Entity <cda_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cda_add_n_bit> (Architecture <for_generate_arch2>) compiled.	Entity <cda_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/data_req.vhd" in Library work.
Entity <data_req> compiled.	Entity <data_req> compiled.
Entity <data_req> (Architecture <rtb>) compiled.	Entity <data_req> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.
Entity <barrel_shifter_sra> compiled.	Entity <up_counter> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.	Entity <up_counter> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.
Entity <CORDIC_ROM> compiled.	Entity <addsub> compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.	Entity <addsub> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/addsub.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/rom.vhd" in Library work.
Entity <addsub> compiled.	Entity <CORDIC_ROM> compiled.
Entity <addsub> (Architecture <ndv>) compiled.	Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/up_counter.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/barrel_shifter_sra.vhd" in Library work.
Entity <up_counter> compiled.	Entity <barrel_shifter_sra> compiled.
Entity <up_counter> (Architecture <rtb>) compiled.	Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic_fsm.vhd" in Library work.
Entity <CORDIC_FSM> compiled.	Entity <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> (Architecture <rtb>) compiled.	Entity <CORDIC_FSM> (Architecture <rtb>) compiled.
Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.	Compiling vhdl file "C:/vhdl/INDV_DN/2024/08_CORDIC/STUDENT/CORDIC_MAIN/cordic.vhd" in Library work.
Entity <cordic> compiled.	Entity <cordic> compiled.
Entity <cordic> (Architecture <rtb>) compiled.	Entity <cordic> (Architecture <rtb>) compiled.
Total REAL time to Xst completion: 0.00 secs	Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.41 secs	Total CPU time to Xst completion: 0.46 secs
->	->
Total memory usage is 4477100 kilobytes	Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)	Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)	Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)	Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb	>fuse -incremental -o CORDIC_logging.tb.isim beh.exe -pri CORDIC_logging.tb.pri -top CORDIC.tb
(Sim P.20131013 (signature 0x708090))	(Sim P.20131013 (signature 0x708090))
Number of CPUs detected in this system: 8	Number of CPUs detected in this system: 8
Turning on multi-threading, number of parallel sub-compilation jobs: 16	Turning on multi-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files	Determining compilation order of HDL files
Parsing VHDL file "cordic_pkv.vhd" into library work	Parsing VHDL file "cordic_pkv.vhd" into library work
Parsing VHDL file "cda_add_n_bit.vhd" into library work	Parsing VHDL file "cda_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter_IDEAL_logging.vhd" into library work	Parsing VHDL file "up_counter_logging.vhd" into library work
Parsing VHDL file "rom_IDEAL_logging.vhd" into library work	Parsing VHDL file "rom_logging.vhd" into library work
Parsing VHDL file "data_req_IDEAL_logging.vhd" into library work	Parsing VHDL file "data_req_logging.vhd" into library work
Parsing VHDL file "cordic_fsm_IDEAL_logging.vhd" into library work	Parsing VHDL file "cordic_fsm_logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra_IDEAL_logging.vhd" into library work	Parsing VHDL file "barrel_shifter_sra_logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work	Parsing VHDL file "addsub.vhd" into library work
Parsing VHDL file "cordic.vhd" into library work	Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work	Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration	Starting static elaboration
Completed static elaboration	Completed static elaboration
Compiling package standard	Compiling package standard
Compiling package textio	Compiling package textio
Compiling package std_logic_1164	Compiling package std_logic_1164
Compiling package numeric_std	Compiling package numeric_std

```

>xst -ifn CORDIC.xst
Release 14.7 - xst P.20131013 (nt64)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
--> Parameter TMPDIR set to ./xst/projnav.tmp
Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.22 secs

--> WARNING:Xst:3164 - Option "-debug" found multiple times in the command line. Only the first occurrence
*           HDL Compilation           *
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENTI/CORDIC_MAIN/cordic_pkg.vhd" in Package <cordic_pkg> compiled.
Package body <cordic_pkg> compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENTI/CORDIC_MAIN/cla_add_n_bit.vhd" in Package <cla_add_n_bit> compiled.
Entity <cla_add_n_bit> (Architecture <for_loop_arch>) compiled.
Entity <cla_add_n_bit> (Architecture <for_generate_arch2>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENTI/CORDIC_MAIN/data_reg.vhd" in Package <data_reg> compiled.
Entity <data_reg> (Architecture <rtl>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENTI/CORDIC_MAIN/up_counter.vhd" in Package <up_counter> compiled.
Entity <up_counter> (Architecture <rtl>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENTI/CORDIC_MAIN/addsub.vhd" in Package <addsub> compiled.
Entity <addsub> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENTI/CORDIC_MAIN/rom.vhd" in Package <CORDIC_ROM> compiled.
Entity <CORDIC_ROM> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENTI/CORDIC_MAIN/barrel_shifter_sra.vhd" in Package <barrel_shifter_sra> compiled.
Entity <barrel_shifter_sra> (Architecture <ndv>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENTI/CORDIC_MAIN/cordic_fsm.vhd" in Package <CORDIC_FSM> compiled.
Entity <CORDIC_FSM> (Architecture <rtl>) compiled.
Compiling vhdl file "C:/vhdl/NDV_DN/2024/08_CORDIC/STUDENTI/CORDIC_MAIN/cordic.vhd" in Package <cordic> compiled.
Entity <cordic> (Architecture <rtl>) compiled.
Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.46 secs

-->
Total memory usage is 4477152 kilobytes
Number of errors : 0 ( 0 filtered)
Number of warnings : 1 ( 0 filtered)
Number of infos : 0 ( 0 filtered)
>fuse -incremental -o CORDIC_logging_tb_isim_beh.exe -prj CORDIC_logging_tb.prj -top CORDIC_tb
ISim P.20131013 (signature 0x7708f090)
Number of CPUs detected in this system: 8
Turning on mult-threading, number of parallel sub-compilation jobs: 16
Determining compilation order of HDL files
Parsing VHDL file "cordic_pkg.vhd" into library work
Parsing VHDL file "cla_add_n_bit.vhd" into library work
Parsing VHDL file "up_counter__logging.vhd" into library work
Parsing VHDL file "rom__logging.vhd" into library work
Parsing VHDL file "data_reg__logging.vhd" into library work
Parsing VHDL file "cordic_fsm__logging.vhd" into library work
Parsing VHDL file "barrel_shifter_sra__logging.vhd" into library work
Parsing VHDL file "addsub.vhd" into library work

```

```

Parsing VHDL file "cordic.vhd" into library work
Parsing VHDL file "CORDIC_TB.vhd" into library work
Starting static elaboration
Completed static elaboration
Compiling package standard
Compiling package textio
Compiling package std_logic_1164
Compiling package numeric_std
Compiling package cordic_pkg
Compiling package math_real
Compiling package std_logic_textio
Compiling architecture rtl of entity data_reg [data_reg(32)]
Compiling architecture rtl of entity up_counter [up_counter(32)]
Compiling architecture for_generate_arch2 of entity cla_add_n_bit [cla_add_n_bit(32)]
Compiling architecture ndv of entity addsub [addsub(32)]
Compiling architecture ndv of entity CORDIC_ROM [CORDIC_ROM(32,32)]
Compiling architecture ndv of entity barrel_shifter_sra [barrel_shifter_sra(32,32)]
Compiling architecture rtl of entity CORDIC_FSM [cordic_fsm_default]
Compiling architecture rtl of entity cordic [cordic(32)]
Compiling architecture testbench_arch of entity cordic_tb
Time Resolution for simulation is 1ps.
Waiting for 1 sub-compilation(s) to finish...
Compiled 24 VHDL Units
Built simulation executable CORDIC_logging_tb_isim_beh.exe
Fuse Memory Usage: 37996 KB
Fuse CPU Usage: 1093 ms
>CORDIC_logging_tb_isim_beh.exe -tclbatch isim.tcl -wdb CORDIC_logging_tb_isim_beh.wdb
ISim P.20131013 (signature 0x7708f090)
WARNING: A WEBPACK license was found.
WARNING: Please use Xilinx License Configuration Manager to check out a full ISim license.
WARNING: ISim will run in Lite mode. Please refer to the ISim documentation for more information on t
This is a Lite version of ISim.
Time resolution is 1 ps
Simulator is doing circuit initialization process.
at 0 ps, Instance /cordic_tb/UUT/LUT/ : Warning: NUMERIC_STD.TO_INTEGER: metavalue detected,
at 0 ps, Instance /cordic_tb/UUT/LUT/ : Warning: NUMERIC_STD.TO_INTEGER: metavalue detected,
at 0 ps: Note: ROM:421657428 (/cordic_tb/UUT/LUT/).
at 0 ps, Instance /cordic_tb/UUT/BARREL_SHIFT_X/ : Warning: NUMERIC_STD.TO_INTEGER: meta
at 0 ps, Instance /cordic_tb/UUT/BARREL_SHIFT_X/ : Warning: NUMERIC_STD.TO_INTEGER: meta
at 0 ps: Note: BARREL SHIFTER:0 (/cordic_tb/UUT/BARREL_SHIFT_X/).
at 0 ps, Instance /cordic_tb/UUT/BARREL_SHIFT_Y/ : Warning: NUMERIC_STD.TO_INTEGER: meta
at 0 ps, Instance /cordic_tb/UUT/BARREL_SHIFT_Y/ : Warning: NUMERIC_STD.TO_INTEGER: meta
at 0 ps: Note: BARREL SHIFTER:0 (/cordic_tb/UUT/BARREL_SHIFT_Y/).
at 0 ps: Note: FSM:IDLE (/cordic_tb/UUT/FSM/).
Finished circuit initialization process.
at 0 ps: Note: ROM:421657428 (/cordic_tb/UUT/LUT/).
at 0 ps, Instance /cordic_tb/UUT/BARREL_SHIFT_X/ : Warning: NUMERIC_STD.TO_INTEGER: meta
at 0 ps: Note: BARREL SHIFTER:0 (/cordic_tb/UUT/BARREL_SHIFT_X/).
at 0 ps, Instance /cordic_tb/UUT/BARREL_SHIFT_Y/ : Warning: NUMERIC_STD.TO_INTEGER: meta
at 0 ps: Note: BARREL SHIFTER:0 (/cordic_tb/UUT/BARREL_SHIFT_Y/).
at 0 ps: Note: FSM:IDLE (/cordic_tb/UUT/FSM/).
at 200 ns(1): Note: DATA REG:326016437 (/cordic_tb/UUT/COS_X_REG/).
at 200 ns(1): Note: DATA REG:0 (/cordic_tb/UUT/SIN_Y_REG/).
at 200 ns(1): Note: DATA REG:0 (/cordic_tb/UUT/ANGLE_Z_REG/).
at 400 ns(1): Note: DATA REG:326016437 (/cordic_tb/UUT/COS_X_REG/).
at 400 ns(1): Note: DATA REG:0 (/cordic_tb/UUT/SIN_Y_REG/).
at 400 ns(1): Note: DATA REG:0 (/cordic_tb/UUT/ANGLE_Z_REG/).
at 600 ns(1): Note: DATA REG:326016437 (/cordic_tb/UUT/COS_X_REG/).
at 600 ns(1): Note: DATA REG:0 (/cordic_tb/UUT/SIN_Y_REG/).

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at 600 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 800 ns(1): Note: FSM:IDLE (/cordic\_tb/UUT/FSM/).  
 at 800 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 800 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 800 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 1 us(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 1 us(1): Note: DATA REG:0 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 1 us(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 1 us(2): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
 at 1200 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 1200 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 1200 ns(1): Note: DATA REG:421657428 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 1200 ns(1): Note: COUNTER:0 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 1200 ns(3): Note: ROM:248918915 (/cordic\_tb/UUT/LUT/).  
 at 1200 ns(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 1200 ns(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 1400 ns(1): Note: DATA REG:489024655 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 1400 ns(1): Note: DATA REG:163008219 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 1400 ns(1): Note: DATA REG:172738513 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 1400 ns(1): Note: COUNTER:1 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 1400 ns(3): Note: ROM:131521918 (/cordic\_tb/UUT/LUT/).  
 at 1400 ns(3): Note: BARREL SHIFTER:122256163 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 1400 ns(3): Note: BARREL SHIFTER:40752054 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 1600 ns(1): Note: DATA REG:529776709 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 1600 ns(1): Note: DATA REG:40752056 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 1600 ns(1): Note: DATA REG:41216595 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 1600 ns(1): Note: COUNTER:2 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 1600 ns(3): Note: ROM:66762579 (/cordic\_tb/UUT/LUT/).  
 at 1600 ns(3): Note: BARREL SHIFTER:66222088 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 1600 ns(3): Note: BARREL SHIFTER:5094007 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 1800 ns(1): Note: DATA REG:534870716 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 1800 ns(1): Note: DATA REG:25470032 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 1800 ns(1): Note: DATA REG:25545984 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 1800 ns(1): Note: COUNTER:3 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 1800 ns(3): Note: ROM:33510843 (/cordic\_tb/UUT/LUT/).  
 at 1800 ns(3): Note: BARREL SHIFTER:33429419 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 1800 ns(3): Note: BARREL SHIFTER:1591877 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 2 us(1): Note: DATA REG:536462593 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 2 us(1): Note: DATA REG:7959387 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 2 us(1): Note: DATA REG:7964859 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 2 us(1): Note: COUNTER:4 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 2 us(3): Note: ROM:16771758 (/cordic\_tb/UUT/LUT/).  
 at 2 us(3): Note: BARREL SHIFTER:16764456 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 2 us(3): Note: BARREL SHIFTER:248730 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 2200 ns(1): Note: DATA REG:536711323 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 2200 ns(1): Note: DATA REG:8805069 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 2200 ns(1): Note: DATA REG:8806899 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 2200 ns(1): Note: COUNTER:5 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 2200 ns(3): Note: ROM:8387925 (/cordic\_tb/UUT/LUT/).  
 at 2200 ns(3): Note: BARREL SHIFTER:8386114 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 2200 ns(3): Note: BARREL SHIFTER:137580 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 2400 ns(1): Note: DATA REG:536848903 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 2400 ns(1): Note: DATA REG:418955 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 2400 ns(1): Note: DATA REG:418974 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 2400 ns(1): Note: COUNTER:6 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 2400 ns(3): Note: ROM:4194219 (/cordic\_tb/UUT/LUT/).  
 at 2400 ns(3): Note: BARREL SHIFTER:4194132 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 2400 ns(3): Note: BARREL SHIFTER:3274 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 2600 ns(1): Note: DATA REG:536852177 (/cordic\_tb/UUT/COS\_X\_REG/).

at 2600 ns(1): Note: DATA REG:3775177 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 2600 ns(1): Note: DATA REG:3775245 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 2600 ns(1): Note: COUNTER:7 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 2600 ns(3): Note: ROM:2097141 (/cordic\_tb/UUT/LUT/).  
 at 2600 ns(3): Note: BARREL SHIFTER:2097078 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 2600 ns(3): Note: BARREL SHIFTER:14746 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 2800 ns(1): Note: DATA REG:536866923 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 2800 ns(1): Note: DATA REG:1678099 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 2800 ns(1): Note: DATA REG:1678104 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 2800 ns(1): Note: COUNTER:8 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 2800 ns(3): Note: ROM:1048575 (/cordic\_tb/UUT/LUT/).  
 at 2800 ns(3): Note: BARREL SHIFTER:1048568 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 2800 ns(3): Note: BARREL SHIFTER:3277 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 3 us(1): Note: DATA REG:536870200 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 3 us(1): Note: DATA REG:629531 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 3 us(1): Note: DATA REG:629529 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 3 us(1): Note: COUNTER:9 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 3 us(3): Note: ROM:524288 (/cordic\_tb/UUT/LUT/).  
 at 3 us(3): Note: BARREL SHIFTER:524287 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 3 us(3): Note: BARREL SHIFTER:614 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 3200 ns(1): Note: DATA REG:536870814 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 3200 ns(1): Note: DATA REG:105244 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 3200 ns(1): Note: DATA REG:105241 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 3200 ns(1): Note: COUNTER:10 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 3200 ns(3): Note: ROM:262144 (/cordic\_tb/UUT/LUT/).  
 at 3200 ns(3): Note: BARREL SHIFTER:262143 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 3200 ns(3): Note: BARREL SHIFTER:51 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 3400 ns(1): Note: DATA REG:536870865 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 3400 ns(1): Note: DATA REG:156899 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 3400 ns(1): Note: DATA REG:156903 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 3400 ns(1): Note: COUNTER:11 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 3400 ns(3): Note: ROM:131072 (/cordic\_tb/UUT/LUT/).  
 at 3400 ns(3): Note: BARREL SHIFTER:131071 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 3400 ns(3): Note: BARREL SHIFTER:39 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 3600 ns(1): Note: DATA REG:536870904 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 3600 ns(1): Note: DATA REG:25828 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 3600 ns(1): Note: DATA REG:25831 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 3600 ns(1): Note: COUNTER:12 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 3600 ns(3): Note: ROM:65536 (/cordic\_tb/UUT/LUT/).  
 at 3600 ns(3): Note: BARREL SHIFTER:65535 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 3600 ns(3): Note: BARREL SHIFTER:4 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 3800 ns(1): Note: DATA REG:536870908 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 3800 ns(1): Note: DATA REG:39707 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 3800 ns(1): Note: DATA REG:39705 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 3800 ns(1): Note: COUNTER:13 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 3800 ns(3): Note: ROM:32768 (/cordic\_tb/UUT/LUT/).  
 at 3800 ns(3): Note: BARREL SHIFTER:32767 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 3800 ns(3): Note: BARREL SHIFTER:2 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 4 us(1): Note: DATA REG:536870910 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 4 us(1): Note: DATA REG:6940 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 4 us(1): Note: DATA REG:6937 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 4 us(1): Note: COUNTER:14 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 4 us(3): Note: ROM:16384 (/cordic\_tb/UUT/LUT/).  
 at 4 us(3): Note: BARREL SHIFTER:16383 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 4 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 4200 ns(1): Note: DATA REG:536870910 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 4200 ns(1): Note: DATA REG:9443 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 4200 ns(1): Note: DATA REG:9447 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 4200 ns(1): Note: COUNTER:15 (/cordic\_tb/UUT/CORDIC\_COUNTER/).



at 4200 ns(3): Note: ROM:8192 (/cordic\_tb/UUT/LUT/).  
 at 4200 ns(3): Note: BARREL SHIFTER:8191 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 4200 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 4400 ns(1): Note: DATA REG:536870911 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 4400 ns(1): Note: DATA REG:1252 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 4400 ns(1): Note: DATA REG:1255 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 4400 ns(1): Note: COUNTER:16 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 4400 ns(3): Note: ROM:4096 (/cordic\_tb/UUT/LUT/).  
 at 4400 ns(3): Note: BARREL SHIFTER:4095 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 4400 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 4600 ns(1): Note: DATA REG:536870912 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 4600 ns(1): Note: DATA REG:2843 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 4600 ns(1): Note: DATA REG:2841 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 4600 ns(1): Note: COUNTER:17 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 4600 ns(3): Note: ROM:2048 (/cordic\_tb/UUT/LUT/).  
 at 4600 ns(3): Note: BARREL SHIFTER:2048 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 4600 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 4800 ns(1): Note: DATA REG:536870912 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 4800 ns(1): Note: DATA REG:795 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 4800 ns(1): Note: DATA REG:793 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 4800 ns(1): Note: COUNTER:18 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 4800 ns(3): Note: ROM:1024 (/cordic\_tb/UUT/LUT/).  
 at 4800 ns(3): Note: BARREL SHIFTER:1024 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 4800 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 5 us(1): Note: DATA REG:536870912 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 5 us(1): Note: DATA REG:-229 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 5 us(1): Note: DATA REG:231 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 5 us(1): Note: COUNTER:19 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 5 us(3): Note: ROM:512 (/cordic\_tb/UUT/LUT/).  
 at 5 us(3): Note: BARREL SHIFTER:512 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 5 us(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 5200 ns(1): Note: DATA REG:536870913 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 5200 ns(1): Note: DATA REG:283 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 5200 ns(1): Note: DATA REG:281 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 5200 ns(1): Note: COUNTER:20 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 5200 ns(3): Note: ROM:256 (/cordic\_tb/UUT/LUT/).  
 at 5200 ns(3): Note: BARREL SHIFTER:256 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 5200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 5400 ns(1): Note: DATA REG:536870913 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 5400 ns(1): Note: DATA REG:27 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 5400 ns(1): Note: DATA REG:25 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 5400 ns(1): Note: COUNTER:21 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 5400 ns(3): Note: ROM:128 (/cordic\_tb/UUT/LUT/).  
 at 5400 ns(3): Note: BARREL SHIFTER:128 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 5400 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 5600 ns(1): Note: DATA REG:536870913 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 5600 ns(1): Note: DATA REG:101 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 5600 ns(1): Note: DATA REG:103 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 5600 ns(1): Note: COUNTER:22 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 5600 ns(3): Note: ROM:64 (/cordic\_tb/UUT/LUT/).  
 at 5600 ns(3): Note: BARREL SHIFTER:64 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 5600 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 5800 ns(1): Note: DATA REG:536870914 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 5800 ns(1): Note: DATA REG:37 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 5800 ns(1): Note: DATA REG:39 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 5800 ns(1): Note: COUNTER:23 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 5800 ns(3): Note: ROM:32 (/cordic\_tb/UUT/LUT/).  
 at 5800 ns(3): Note: BARREL SHIFTER:32 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 5800 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 6 us(1): Note: DATA REG:536870915 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 6 us(1): Note: DATA REG:-5 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 6 us(1): Note: DATA REG:7 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 6 us(1): Note: COUNTER:24 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 6 us(3): Note: ROM:16 (/cordic\_tb/UUT/LUT/).  
 at 6 us(3): Note: BARREL SHIFTER:16 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 6 us(3): Note: BARREL SHIFTER:-1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 6200 ns(1): Note: DATA REG:536870916 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 6200 ns(1): Note: DATA REG:11 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 6200 ns(1): Note: DATA REG:-9 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 6200 ns(1): Note: COUNTER:25 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 6200 ns(3): Note: ROM:8 (/cordic\_tb/UUT/LUT/).  
 at 6200 ns(3): Note: BARREL SHIFTER:8 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 6200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 6400 ns(1): Note: DATA REG:536870916 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 6400 ns(1): Note: DATA REG:3 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 6400 ns(1): Note: DATA REG:-1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 6400 ns(1): Note: COUNTER:26 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 6400 ns(3): Note: ROM:4 (/cordic\_tb/UUT/LUT/).  
 at 6400 ns(3): Note: BARREL SHIFTER:4 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 6400 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 6600 ns(1): Note: DATA REG:536870916 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 6600 ns(1): Note: DATA REG:-1 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 6600 ns(1): Note: DATA REG:3 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 6600 ns(1): Note: COUNTER:27 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 6600 ns(3): Note: ROM:2 (/cordic\_tb/UUT/LUT/).  
 at 6600 ns(3): Note: BARREL SHIFTER:2 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 6600 ns(3): Note: BARREL SHIFTER:-1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 6800 ns(1): Note: DATA REG:536870917 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 6800 ns(1): Note: DATA REG:1 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 6800 ns(1): Note: DATA REG:1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 6800 ns(1): Note: COUNTER:28 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 6800 ns(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
 at 6800 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 6800 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 7 us(1): Note: DATA REG:536870917 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 7 us(1): Note: DATA REG:2 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 7 us(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 7 us(1): Note: COUNTER:29 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 7 us(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
 at 7 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 7 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 7200 ns(1): Note: DATA REG:536870917 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 7200 ns(1): Note: DATA REG:2 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 7200 ns(1): Note: DATA REG:-1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 7200 ns(1): Note: COUNTER:30 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 7200 ns(3): Note: ROM:0 (/cordic\_tb/UUT/LUT/).  
 at 7200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 7200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 7200 ns(3): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
 at 7400 ns(1): Note: DATA REG:536870917 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 7400 ns(1): Note: DATA REG:2 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 7400 ns(1): Note: DATA REG:-1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 7400 ns(1): Note: COUNTER:31 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 7400 ns(2): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).  
 at 7400 ns(3): Note: ROM:421657428 (/cordic\_tb/UUT/LUT/).  
 at 7400 ns(3): Note: BARREL SHIFTER:536870917 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 7400 ns(3): Note: BARREL SHIFTER:2 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 7400 ns(3): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).

at 7800 ns(2): Note: FSM:IDLE (/cordic\_tb/UUT/FSM/).  
 at 8 us(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 8 us(1): Note: DATA REG:0 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 8 us(1): Note: DATA REG:843314857 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 8 us(2): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
 at 8200 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 8200 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 8200 ns(1): Note: DATA REG:421657429 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 8200 ns(1): Note: COUNTER:0 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 8200 ns(3): Note: ROM:248918915 (/cordic\_tb/UUT/LUT/).  
 at 8200 ns(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 8200 ns(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 8400 ns(1): Note: DATA REG:163008219 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 8400 ns(1): Note: DATA REG:489024655 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 8400 ns(1): Note: DATA REG:172738514 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 8400 ns(1): Note: COUNTER:1 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 8400 ns(3): Note: ROM:131521918 (/cordic\_tb/UUT/LUT/).  
 at 8400 ns(3): Note: BARREL SHIFTER:40752054 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 8400 ns(3): Note: BARREL SHIFTER:122256163 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 8600 ns(1): Note: DATA REG:40752056 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 8600 ns(1): Note: DATA REG:529776709 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 8600 ns(1): Note: DATA REG:41216596 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 8600 ns(1): Note: COUNTER:2 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 8600 ns(3): Note: ROM:66762579 (/cordic\_tb/UUT/LUT/).  
 at 8600 ns(3): Note: BARREL SHIFTER:5094007 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 8600 ns(3): Note: BARREL SHIFTER:66222088 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 8800 ns(1): Note: DATA REG:25470032 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 8800 ns(1): Note: DATA REG:534870716 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 8800 ns(1): Note: DATA REG:25545983 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 8800 ns(1): Note: COUNTER:3 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 8800 ns(3): Note: ROM:33510843 (/cordic\_tb/UUT/LUT/).  
 at 8800 ns(3): Note: BARREL SHIFTER:1591877 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 8800 ns(3): Note: BARREL SHIFTER:33429419 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 9 us(1): Note: DATA REG:7959387 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 9 us(1): Note: DATA REG:536462593 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 9 us(1): Note: DATA REG:7964860 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 9 us(1): Note: COUNTER:4 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 9 us(3): Note: ROM:16771758 (/cordic\_tb/UUT/LUT/).  
 at 9 us(3): Note: BARREL SHIFTER:248730 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 9 us(3): Note: BARREL SHIFTER:16764456 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 9200 ns(1): Note: DATA REG:8805069 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 9200 ns(1): Note: DATA REG:536711323 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 9200 ns(1): Note: DATA REG:8806898 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 9200 ns(1): Note: COUNTER:5 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 9200 ns(3): Note: ROM:8387925 (/cordic\_tb/UUT/LUT/).  
 at 9200 ns(3): Note: BARREL SHIFTER:137580 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 9200 ns(3): Note: BARREL SHIFTER:8386114 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 9400 ns(1): Note: DATA REG:418955 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 9400 ns(1): Note: DATA REG:536848903 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 9400 ns(1): Note: DATA REG:418973 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 9400 ns(1): Note: COUNTER:6 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 9400 ns(3): Note: ROM:4194219 (/cordic\_tb/UUT/LUT/).  
 at 9400 ns(3): Note: BARREL SHIFTER:3274 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 9400 ns(3): Note: BARREL SHIFTER:4194132 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 9600 ns(1): Note: DATA REG:3775177 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 9600 ns(1): Note: DATA REG:536852177 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 9600 ns(1): Note: DATA REG:3775246 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 9600 ns(1): Note: COUNTER:7 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 9600 ns(3): Note: ROM:2097141 (/cordic\_tb/UUT/LUT/).

at 9600 ns(3): Note: BARREL SHIFTER:14746 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 9600 ns(3): Note: BARREL SHIFTER:2097078 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 9800 ns(1): Note: DATA REG:1678099 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 9800 ns(1): Note: DATA REG:536866923 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 9800 ns(1): Note: DATA REG:1678105 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 9800 ns(1): Note: COUNTER:8 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 9800 ns(3): Note: ROM:1048575 (/cordic\_tb/UUT/LUT/).  
 at 9800 ns(3): Note: BARREL SHIFTER:3277 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 9800 ns(3): Note: BARREL SHIFTER:1048568 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 10 us(1): Note: DATA REG:629531 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 10 us(1): Note: DATA REG:536870200 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 10 us(1): Note: DATA REG:629530 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 10 us(1): Note: COUNTER:9 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 10 us(3): Note: ROM:524288 (/cordic\_tb/UUT/LUT/).  
 at 10 us(3): Note: BARREL SHIFTER:614 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 10 us(3): Note: BARREL SHIFTER:524287 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 10200 ns(1): Note: DATA REG:105244 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 10200 ns(1): Note: DATA REG:536870814 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 10200 ns(1): Note: DATA REG:105242 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 10200 ns(1): Note: COUNTER:10 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 10200 ns(3): Note: ROM:262144 (/cordic\_tb/UUT/LUT/).  
 at 10200 ns(3): Note: BARREL SHIFTER:51 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 10200 ns(3): Note: BARREL SHIFTER:262143 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 10400 ns(1): Note: DATA REG:-156899 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 10400 ns(1): Note: DATA REG:536870865 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 10400 ns(1): Note: DATA REG:-156902 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 10400 ns(1): Note: COUNTER:11 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 10400 ns(3): Note: ROM:131072 (/cordic\_tb/UUT/LUT/).  
 at 10400 ns(3): Note: BARREL SHIFTER:39 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 10400 ns(3): Note: BARREL SHIFTER:131071 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 10600 ns(1): Note: DATA REG:-25828 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 10600 ns(1): Note: DATA REG:536870904 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 10600 ns(1): Note: DATA REG:-25830 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 10600 ns(1): Note: COUNTER:12 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 10600 ns(3): Note: ROM:65536 (/cordic\_tb/UUT/LUT/).  
 at 10600 ns(3): Note: BARREL SHIFTER:4 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 10600 ns(3): Note: BARREL SHIFTER:65535 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 10800 ns(1): Note: DATA REG:39707 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 10800 ns(1): Note: DATA REG:536870908 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 10800 ns(1): Note: DATA REG:39706 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 10800 ns(1): Note: COUNTER:13 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 10800 ns(3): Note: ROM:32768 (/cordic\_tb/UUT/LUT/).  
 at 10800 ns(3): Note: BARREL SHIFTER:2 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 10800 ns(3): Note: BARREL SHIFTER:32767 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 11 us(1): Note: DATA REG:6940 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 11 us(1): Note: DATA REG:536870910 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 11 us(1): Note: DATA REG:6938 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 11 us(1): Note: COUNTER:14 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 11 us(3): Note: ROM:16384 (/cordic\_tb/UUT/LUT/).  
 at 11 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 11 us(3): Note: BARREL SHIFTER:16383 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 11200 ns(1): Note: DATA REG:-9443 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 11200 ns(1): Note: DATA REG:536870910 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 11200 ns(1): Note: DATA REG:-9446 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 11200 ns(1): Note: COUNTER:15 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 11200 ns(3): Note: ROM:8192 (/cordic\_tb/UUT/LUT/).  
 at 11200 ns(3): Note: BARREL SHIFTER:-1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 11200 ns(3): Note: BARREL SHIFTER:8191 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 11400 ns(1): Note: DATA REG:-1252 (/cordic\_tb/UUT/COS\_X\_REG/).

at 11400 ns(1): Note: DATA REG:536870911 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 11400 ns(1): Note: DATA REG:-1254 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 11400 ns(1): Note: COUNTER:16 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 11400 ns(3): Note: ROM:4096 (/cordic\_tb/UUT/LUT/).  
 at 11400 ns(3): Note: BARREL SHIFTER:-1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 11400 ns(3): Note: BARREL SHIFTER:4095 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 11600 ns(1): Note: DATA REG:2843 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 11600 ns(1): Note: DATA REG:536870912 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 11600 ns(1): Note: DATA REG:2842 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 11600 ns(1): Note: COUNTER:17 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 11600 ns(3): Note: ROM:2048 (/cordic\_tb/UUT/LUT/).  
 at 11600 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 11600 ns(3): Note: BARREL SHIFTER:2048 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 11800 ns(1): Note: DATA REG:795 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 11800 ns(1): Note: DATA REG:536870912 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 11800 ns(1): Note: DATA REG:794 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 11800 ns(1): Note: COUNTER:18 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 11800 ns(3): Note: ROM:1024 (/cordic\_tb/UUT/LUT/).  
 at 11800 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 11800 ns(3): Note: BARREL SHIFTER:1024 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 12 us(1): Note: DATA REG:-229 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 12 us(1): Note: DATA REG:536870912 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 12 us(1): Note: DATA REG:-230 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 12 us(1): Note: COUNTER:19 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 12 us(3): Note: ROM:512 (/cordic\_tb/UUT/LUT/).  
 at 12 us(3): Note: BARREL SHIFTER:-1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 12 us(3): Note: BARREL SHIFTER:512 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 12200 ns(1): Note: DATA REG:283 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 12200 ns(1): Note: DATA REG:536870913 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 12200 ns(1): Note: DATA REG:282 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 12200 ns(1): Note: COUNTER:20 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 12200 ns(3): Note: ROM:256 (/cordic\_tb/UUT/LUT/).  
 at 12200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 12200 ns(3): Note: BARREL SHIFTER:256 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 12400 ns(1): Note: DATA REG:27 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 12400 ns(1): Note: DATA REG:536870913 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 12400 ns(1): Note: DATA REG:26 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 12400 ns(1): Note: COUNTER:21 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 12400 ns(3): Note: ROM:128 (/cordic\_tb/UUT/LUT/).  
 at 12400 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 12400 ns(3): Note: BARREL SHIFTER:128 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 12600 ns(1): Note: DATA REG:-101 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 12600 ns(1): Note: DATA REG:536870913 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 12600 ns(1): Note: DATA REG:-102 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 12600 ns(1): Note: COUNTER:22 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 12600 ns(3): Note: ROM:64 (/cordic\_tb/UUT/LUT/).  
 at 12600 ns(3): Note: BARREL SHIFTER:-1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 12600 ns(3): Note: BARREL SHIFTER:64 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 12800 ns(1): Note: DATA REG:-37 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 12800 ns(1): Note: DATA REG:536870914 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 12800 ns(1): Note: DATA REG:-38 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 12800 ns(1): Note: COUNTER:23 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 12800 ns(3): Note: ROM:32 (/cordic\_tb/UUT/LUT/).  
 at 12800 ns(3): Note: BARREL SHIFTER:-1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 12800 ns(3): Note: BARREL SHIFTER:32 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 13 us(1): Note: DATA REG:-5 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 13 us(1): Note: DATA REG:536870915 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 13 us(1): Note: DATA REG:-6 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 13 us(1): Note: COUNTER:24 (/cordic\_tb/UUT/CORDIC\_COUNTER/).

at 13 us(3): Note: ROM:16 (/cordic\_tb/UUT/LUT/).  
 at 13 us(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 13 us(3): Note: BARREL SHIFTER:16 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 13200 ns(1): Note: DATA REG:11 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 13200 ns(1): Note: DATA REG:536870916 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 13200 ns(1): Note: DATA REG:10 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 13200 ns(1): Note: COUNTER:25 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 13200 ns(3): Note: ROM:8 (/cordic\_tb/UUT/LUT/).  
 at 13200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 13200 ns(3): Note: BARREL SHIFTER:8 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 13400 ns(1): Note: DATA REG:3 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 13400 ns(1): Note: DATA REG:536870916 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 13400 ns(1): Note: DATA REG:2 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 13400 ns(1): Note: COUNTER:26 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 13400 ns(3): Note: ROM:4 (/cordic\_tb/UUT/LUT/).  
 at 13400 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 13400 ns(3): Note: BARREL SHIFTER:4 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 13600 ns(1): Note: DATA REG:1 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 13600 ns(1): Note: DATA REG:536870916 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 13600 ns(1): Note: DATA REG:2 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 13600 ns(1): Note: COUNTER:27 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 13600 ns(3): Note: ROM:2 (/cordic\_tb/UUT/LUT/).  
 at 13600 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 13600 ns(3): Note: BARREL SHIFTER:2 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 13800 ns(1): Note: DATA REG:1 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 13800 ns(1): Note: DATA REG:536870917 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 13800 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 13800 ns(1): Note: COUNTER:28 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 13800 ns(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
 at 13800 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 13800 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 14 us(1): Note: DATA REG:0 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 14 us(1): Note: DATA REG:536870917 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 14 us(1): Note: DATA REG:1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 14 us(1): Note: COUNTER:29 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 14 us(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
 at 14 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 14 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 14200 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 14200 ns(1): Note: DATA REG:536870917 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 14200 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 14200 ns(1): Note: COUNTER:30 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 14200 ns(3): Note: ROM:0 (/cordic\_tb/UUT/LUT/).  
 at 14200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 14200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 14200 ns(3): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
 at 14400 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 14400 ns(1): Note: DATA REG:536870917 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 14400 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 14400 ns(1): Note: COUNTER:31 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 14400 ns(2): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).  
 at 14400 ns(3): Note: ROM:421657428 (/cordic\_tb/UUT/LUT/).  
 at 14400 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 14400 ns(3): Note: BARREL SHIFTER:536870917 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 14400 ns(3): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).  
 at 15200 ns(2): Note: FSM:IDLE (/cordic\_tb/UUT/FSM/).  
 at 15400 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 15400 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 15400 ns(1): Note: DATA REG:562209904 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 15400 ns(2): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
 at 15600 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 15600 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 15600 ns(1): Note: DATA REG:140552476 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 15600 ns(1): Note: COUNTER:0 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 15600 ns(3): Note: ROM:248918915 (/cordic\_tb/UUT/LUT/).  
 at 15600 ns(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 15600 ns(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 15800 ns(1): Note: DATA REG:163008219 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 15800 ns(1): Note: DATA REG:489024655 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 15800 ns(1): Note: DATA REG:-108366439 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 15800 ns(1): Note: COUNTER:1 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 15800 ns(3): Note: ROM:131521918 (/cordic\_tb/UUT/LUT/).  
 at 15800 ns(3): Note: BARREL SHIFTER:40752054 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 15800 ns(3): Note: BARREL SHIFTER:122256163 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 16 us(1): Note: DATA REG:285264382 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 16 us(1): Note: DATA REG:448272601 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 16 us(1): Note: DATA REG:23155479 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 16 us(1): Note: COUNTER:2 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 16 us(3): Note: ROM:66762579 (/cordic\_tb/UUT/LUT/).  
 at 16 us(3): Note: BARREL SHIFTER:35658047 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 16 us(3): Note: BARREL SHIFTER:56034075 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 16200 ns(1): Note: DATA REG:229230307 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 16200 ns(1): Note: DATA REG:483930648 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 16200 ns(1): Note: DATA REG:-43607100 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 16200 ns(1): Note: COUNTER:3 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 16200 ns(3): Note: ROM:33510843 (/cordic\_tb/UUT/LUT/).  
 at 16200 ns(3): Note: BARREL SHIFTER:14326894 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 16200 ns(3): Note: BARREL SHIFTER:30245665 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 16400 ns(1): Note: DATA REG:259475972 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 16400 ns(1): Note: DATA REG:469603754 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 16400 ns(1): Note: DATA REG:-10096257 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 16400 ns(1): Note: COUNTER:4 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 16400 ns(3): Note: ROM:16771758 (/cordic\_tb/UUT/LUT/).  
 at 16400 ns(3): Note: BARREL SHIFTER:8108624 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 16400 ns(3): Note: BARREL SHIFTER:14675117 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 16600 ns(1): Note: DATA REG:274151089 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 16600 ns(1): Note: DATA REG:461495130 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 16600 ns(1): Note: DATA REG:6675501 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 16600 ns(1): Note: COUNTER:5 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 16600 ns(3): Note: ROM:8387925 (/cordic\_tb/UUT/LUT/).  
 at 16600 ns(3): Note: BARREL SHIFTER:4283610 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 16600 ns(3): Note: BARREL SHIFTER:7210861 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 16800 ns(1): Note: DATA REG:266940228 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 16800 ns(1): Note: DATA REG:465778740 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 16800 ns(1): Note: DATA REG:-1712424 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 16800 ns(1): Note: COUNTER:6 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 16800 ns(3): Note: ROM:4194219 (/cordic\_tb/UUT/LUT/).  
 at 16800 ns(3): Note: BARREL SHIFTER:2085470 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 16800 ns(3): Note: BARREL SHIFTER:3638896 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 17 us(1): Note: DATA REG:270579124 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 17 us(1): Note: DATA REG:463693270 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 17 us(1): Note: DATA REG:2481795 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 17 us(1): Note: COUNTER:7 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 17 us(3): Note: ROM:2097141 (/cordic\_tb/UUT/LUT/).  
 at 17 us(3): Note: BARREL SHIFTER:1056949 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 17 us(3): Note: BARREL SHIFTER:1811301 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 17200 ns(1): Note: DATA REG:268767823 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 17200 ns(1): Note: DATA REG:464750219 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 17200 ns(1): Note: DATA REG:384654 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 17200 ns(1): Note: COUNTER:8 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 17200 ns(3): Note: ROM:1048575 (/cordic\_tb/UUT/LUT/).  
 at 17200 ns(3): Note: BARREL SHIFTER:524937 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 17200 ns(3): Note: BARREL SHIFTER:907715 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 17400 ns(1): Note: DATA REG:267860108 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 17400 ns(1): Note: DATA REG:465275156 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 17400 ns(1): Note: DATA REG:563921 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 17400 ns(1): Note: COUNTER:9 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 17400 ns(3): Note: ROM:524288 (/cordic\_tb/UUT/LUT/).  
 at 17400 ns(3): Note: BARREL SHIFTER:261582 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 17400 ns(3): Note: BARREL SHIFTER:454370 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 17600 ns(1): Note: DATA REG:268314478 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 17600 ns(1): Note: DATA REG:465013574 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 17600 ns(1): Note: DATA REG:139633 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 17600 ns(1): Note: COUNTER:10 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 17600 ns(3): Note: ROM:262144 (/cordic\_tb/UUT/LUT/).  
 at 17600 ns(3): Note: BARREL SHIFTER:131012 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 17600 ns(3): Note: BARREL SHIFTER:227057 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 17800 ns(1): Note: DATA REG:268541535 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 17800 ns(1): Note: DATA REG:464882562 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 17800 ns(1): Note: DATA REG:122511 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 17800 ns(1): Note: COUNTER:11 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 17800 ns(3): Note: ROM:131072 (/cordic\_tb/UUT/LUT/).  
 at 17800 ns(3): Note: BARREL SHIFTER:65561 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 17800 ns(3): Note: BARREL SHIFTER:113496 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 18 us(1): Note: DATA REG:268428039 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 18 us(1): Note: DATA REG:464948123 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 18 us(1): Note: DATA REG:8561 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 18 us(1): Note: COUNTER:12 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 18 us(3): Note: ROM:65536 (/cordic\_tb/UUT/LUT/).  
 at 18 us(3): Note: BARREL SHIFTER:32767 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 18 us(3): Note: BARREL SHIFTER:56756 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 18200 ns(1): Note: DATA REG:268484795 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 18200 ns(1): Note: DATA REG:464915356 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 18200 ns(1): Note: DATA REG:56975 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 18200 ns(1): Note: COUNTER:13 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 18200 ns(3): Note: ROM:32768 (/cordic\_tb/UUT/LUT/).  
 at 18200 ns(3): Note: BARREL SHIFTER:16387 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 18200 ns(3): Note: BARREL SHIFTER:28376 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 18400 ns(1): Note: DATA REG:268456419 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 18400 ns(1): Note: DATA REG:464931743 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 18400 ns(1): Note: DATA REG:24207 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 18400 ns(1): Note: COUNTER:14 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 18400 ns(3): Note: ROM:16384 (/cordic\_tb/UUT/LUT/).  
 at 18400 ns(3): Note: BARREL SHIFTER:8192 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 18400 ns(3): Note: BARREL SHIFTER:14188 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 18600 ns(1): Note: DATA REG:268442231 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 18600 ns(1): Note: DATA REG:464939935 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 18600 ns(1): Note: DATA REG:7823 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 18600 ns(1): Note: COUNTER:15 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 18600 ns(3): Note: ROM:8192 (/cordic\_tb/UUT/LUT/).  
 at 18600 ns(3): Note: BARREL SHIFTER:4096 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 18600 ns(3): Note: BARREL SHIFTER:7094 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 18800 ns(1): Note: DATA REG:268435137 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 18800 ns(1): Note: DATA REG:464944031 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 18800 ns(1): Note: DATA REG:369 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 18800 ns(1): Note: COUNTER:16 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 18800 ns(3): Note: ROM:4096 (/cordic\_tb/UUT/LUT/).



at 18800 ns(3): Note: BARREL SHIFTER:2047 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 18800 ns(3): Note: BARREL SHIFTER:3547 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 19 us(1): Note: DATA REG:268438684 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 19 us(1): Note: DATA REG:464941984 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 19 us(1): Note: DATA REG:3727 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 19 us(1): Note: COUNTER:17 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 19 us(3): Note: ROM:2048 (/cordic\_tb/UUT/LUT/).  
 at 19 us(3): Note: BARREL SHIFTER:1024 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 19 us(3): Note: BARREL SHIFTER:1773 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 19200 ns(1): Note: DATA REG:268436911 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 19200 ns(1): Note: DATA REG:464943008 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 19200 ns(1): Note: DATA REG:1679 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 19200 ns(1): Note: COUNTER:18 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 19200 ns(3): Note: ROM:1024 (/cordic\_tb/UUT/LUT/).  
 at 19200 ns(3): Note: BARREL SHIFTER:512 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 19200 ns(3): Note: BARREL SHIFTER:886 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 19400 ns(1): Note: DATA REG:268436025 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 19400 ns(1): Note: DATA REG:464943520 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 19400 ns(1): Note: DATA REG:655 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 19400 ns(1): Note: COUNTER:19 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 19400 ns(3): Note: ROM:512 (/cordic\_tb/UUT/LUT/).  
 at 19400 ns(3): Note: BARREL SHIFTER:256 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 19400 ns(3): Note: BARREL SHIFTER:443 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 19600 ns(1): Note: DATA REG:268435582 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 19600 ns(1): Note: DATA REG:464943776 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 19600 ns(1): Note: DATA REG:143 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 19600 ns(1): Note: COUNTER:20 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 19600 ns(3): Note: ROM:256 (/cordic\_tb/UUT/LUT/).  
 at 19600 ns(3): Note: BARREL SHIFTER:128 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 19600 ns(3): Note: BARREL SHIFTER:221 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 19800 ns(1): Note: DATA REG:268435361 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 19800 ns(1): Note: DATA REG:464943904 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 19800 ns(1): Note: DATA REG:-113 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 19800 ns(1): Note: COUNTER:21 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 19800 ns(3): Note: ROM:128 (/cordic\_tb/UUT/LUT/).  
 at 19800 ns(3): Note: BARREL SHIFTER:63 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 19800 ns(3): Note: BARREL SHIFTER:110 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 20 us(1): Note: DATA REG:268435471 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 20 us(1): Note: DATA REG:464943841 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 20 us(1): Note: DATA REG:15 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 20 us(1): Note: COUNTER:22 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 20 us(3): Note: ROM:64 (/cordic\_tb/UUT/LUT/).  
 at 20 us(3): Note: BARREL SHIFTER:32 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 20 us(3): Note: BARREL SHIFTER:55 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 20200 ns(1): Note: DATA REG:268435416 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 20200 ns(1): Note: DATA REG:464943873 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 20200 ns(1): Note: DATA REG:-49 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 20200 ns(1): Note: COUNTER:23 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 20200 ns(3): Note: ROM:32 (/cordic\_tb/UUT/LUT/).  
 at 20200 ns(3): Note: BARREL SHIFTER:15 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 20200 ns(3): Note: BARREL SHIFTER:27 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 20400 ns(1): Note: DATA REG:268435443 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 20400 ns(1): Note: DATA REG:464943858 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 20400 ns(1): Note: DATA REG:-17 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 20400 ns(1): Note: COUNTER:24 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 20400 ns(3): Note: ROM:16 (/cordic\_tb/UUT/LUT/).  
 at 20400 ns(3): Note: BARREL SHIFTER:7 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 20400 ns(3): Note: BARREL SHIFTER:13 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 20600 ns(1): Note: DATA REG:268435456 (/cordic\_tb/UUT/COS\_X\_REG/).

at 20600 ns(1): Note: DATA REG:464943851 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 20600 ns(1): Note: DATA REG:-1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 20600 ns(1): Note: COUNTER:25 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 20600 ns(3): Note: ROM:8 (/cordic\_tb/UUT/LUT/).  
 at 20600 ns(3): Note: BARREL SHIFTER:4 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 20600 ns(3): Note: BARREL SHIFTER:6 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 20800 ns(1): Note: DATA REG:268435462 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 20800 ns(1): Note: DATA REG:464943847 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 20800 ns(1): Note: DATA REG:7 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 20800 ns(1): Note: COUNTER:26 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 20800 ns(3): Note: ROM:4 (/cordic\_tb/UUT/LUT/).  
 at 20800 ns(3): Note: BARREL SHIFTER:2 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 20800 ns(3): Note: BARREL SHIFTER:3 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 21 us(1): Note: DATA REG:268435459 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 21 us(1): Note: DATA REG:464943849 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 21 us(1): Note: DATA REG:3 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 21 us(1): Note: COUNTER:27 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 21 us(3): Note: ROM:2 (/cordic\_tb/UUT/LUT/).  
 at 21 us(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 21 us(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 21200 ns(1): Note: DATA REG:268435458 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 21200 ns(1): Note: DATA REG:464943850 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 21200 ns(1): Note: DATA REG:1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 21200 ns(1): Note: COUNTER:28 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 21200 ns(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
 at 21200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 21200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 21400 ns(1): Note: DATA REG:268435458 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 21400 ns(1): Note: DATA REG:464943850 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 21400 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 21400 ns(1): Note: COUNTER:29 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 21400 ns(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
 at 21400 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 21400 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 21600 ns(1): Note: DATA REG:268435458 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 21600 ns(1): Note: DATA REG:464943850 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 21600 ns(1): Note: DATA REG:-1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 21600 ns(1): Note: COUNTER:30 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 21600 ns(3): Note: ROM:0 (/cordic\_tb/UUT/LUT/).  
 at 21600 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 21600 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 21600 ns(3): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
 at 21800 ns(1): Note: DATA REG:268435458 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 21800 ns(1): Note: DATA REG:464943850 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 21800 ns(1): Note: DATA REG:-1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 21800 ns(1): Note: COUNTER:31 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 21800 ns(2): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).  
 at 21800 ns(3): Note: ROM:421657428 (/cordic\_tb/UUT/LUT/).  
 at 21800 ns(3): Note: BARREL SHIFTER:268435458 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 21800 ns(3): Note: BARREL SHIFTER:464943850 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 21800 ns(3): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).  
 at 22600 ns(2): Note: FSM:IDLE (/cordic\_tb/UUT/FSM/).  
 at 22800 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 22800 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 22800 ns(1): Note: DATA REG:421657428 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 22800 ns(2): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
 at 23 us(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 23 us(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 23 us(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 23 us(1): Note: COUNTER:0 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 23 us(3): Note: ROM:248918915 (/cordic\_tb/UUT/LUT/).  
 at 23 us(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 23 us(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 23200 ns(1): Note: DATA REG:163008219 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 23200 ns(1): Note: DATA REG:489024655 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 23200 ns(1): Note: DATA REG:-248918915 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 23200 ns(1): Note: COUNTER:1 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 23200 ns(3): Note: ROM:131521918 (/cordic\_tb/UUT/LUT/).  
 at 23200 ns(3): Note: BARREL SHIFTER:40752054 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 23200 ns(3): Note: BARREL SHIFTER:122256163 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 23400 ns(1): Note: DATA REG:285264382 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 23400 ns(1): Note: DATA REG:448272601 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 23400 ns(1): Note: DATA REG:-117396997 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 23400 ns(1): Note: COUNTER:2 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 23400 ns(3): Note: ROM:66762579 (/cordic\_tb/UUT/LUT/).  
 at 23400 ns(3): Note: BARREL SHIFTER:35658047 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 23400 ns(3): Note: BARREL SHIFTER:56034075 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 23600 ns(1): Note: DATA REG:341298457 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 23600 ns(1): Note: DATA REG:412614554 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 23600 ns(1): Note: DATA REG:-50634418 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 23600 ns(1): Note: COUNTER:3 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 23600 ns(3): Note: ROM:33510843 (/cordic\_tb/UUT/LUT/).  
 at 23600 ns(3): Note: BARREL SHIFTER:21331153 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 23600 ns(3): Note: BARREL SHIFTER:25788409 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 23800 ns(1): Note: DATA REG:367086866 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 23800 ns(1): Note: DATA REG:391283401 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 23800 ns(1): Note: DATA REG:-17123575 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 23800 ns(1): Note: COUNTER:4 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 23800 ns(3): Note: ROM:16771758 (/cordic\_tb/UUT/LUT/).  
 at 23800 ns(3): Note: BARREL SHIFTER:11471464 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 23800 ns(3): Note: BARREL SHIFTER:12227606 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 24 us(1): Note: DATA REG:379314472 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 24 us(1): Note: DATA REG:379811937 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 24 us(1): Note: DATA REG:-351817 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 24 us(1): Note: COUNTER:5 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 24 us(3): Note: ROM:8387925 (/cordic\_tb/UUT/LUT/).  
 at 24 us(3): Note: BARREL SHIFTER:5926788 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 24 us(3): Note: BARREL SHIFTER:5934561 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 24200 ns(1): Note: DATA REG:385249033 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 24200 ns(1): Note: DATA REG:373885149 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 24200 ns(1): Note: DATA REG:8036108 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 24200 ns(1): Note: COUNTER:6 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 24200 ns(3): Note: ROM:4194219 (/cordic\_tb/UUT/LUT/).  
 at 24200 ns(3): Note: BARREL SHIFTER:3009758 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 24200 ns(3): Note: BARREL SHIFTER:2920977 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 24400 ns(1): Note: DATA REG:382328056 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 24400 ns(1): Note: DATA REG:376894907 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 24400 ns(1): Note: DATA REG:3841889 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 24400 ns(1): Note: COUNTER:7 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 24400 ns(3): Note: ROM:2097141 (/cordic\_tb/UUT/LUT/).  
 at 24400 ns(3): Note: BARREL SHIFTER:1493468 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 24400 ns(3): Note: BARREL SHIFTER:1472245 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 24600 ns(1): Note: DATA REG:380855811 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 24600 ns(1): Note: DATA REG:378388375 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 24600 ns(1): Note: DATA REG:1744748 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 24600 ns(1): Note: COUNTER:8 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 24600 ns(3): Note: ROM:1048575 (/cordic\_tb/UUT/LUT/).  
 at 24600 ns(3): Note: BARREL SHIFTER:743859 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).

at 24600 ns(3): Note: BARREL SHIFTER:739039 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 24800 ns(1): Note: DATA REG:380116772 (/cordic\_tb/UUT/COS\_X\_REG/).

at 24800 ns(1): Note: DATA REG:379132234 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 24800 ns(1): Note: DATA REG:696173 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 24800 ns(1): Note: COUNTER:9 (/cordic\_tb/UUT/CORDIC\_COUNTER/).

at 24800 ns(3): Note: ROM:524288 (/cordic\_tb/UUT/LUT/).

at 24800 ns(3): Note: BARREL SHIFTER:371207 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).

at 24800 ns(3): Note: BARREL SHIFTER:370246 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 25 us(1): Note: DATA REG:379746526 (/cordic\_tb/UUT/COS\_X\_REG/).

at 25 us(1): Note: DATA REG:379503441 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 25 us(1): Note: DATA REG:171885 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 25 us(1): Note: COUNTER:10 (/cordic\_tb/UUT/CORDIC\_COUNTER/).

at 25 us(3): Note: ROM:262144 (/cordic\_tb/UUT/LUT/).

at 25 us(3): Note: BARREL SHIFTER:185423 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).

at 25 us(3): Note: BARREL SHIFTER:185304 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 25200 ns(1): Note: DATA REG:379561222 (/cordic\_tb/UUT/COS\_X\_REG/).

at 25200 ns(1): Note: DATA REG:379688864 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 25200 ns(1): Note: DATA REG:90259 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 25200 ns(1): Note: COUNTER:11 (/cordic\_tb/UUT/CORDIC\_COUNTER/).

at 25200 ns(3): Note: ROM:131072 (/cordic\_tb/UUT/LUT/).

at 25200 ns(3): Note: BARREL SHIFTER:92666 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).

at 25200 ns(3): Note: BARREL SHIFTER:92697 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 25400 ns(1): Note: DATA REG:379653919 (/cordic\_tb/UUT/COS\_X\_REG/).

at 25400 ns(1): Note: DATA REG:379596198 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 25400 ns(1): Note: DATA REG:40813 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 25400 ns(1): Note: COUNTER:12 (/cordic\_tb/UUT/CORDIC\_COUNTER/).

at 25400 ns(3): Note: ROM:65536 (/cordic\_tb/UUT/LUT/).

at 25400 ns(3): Note: BARREL SHIFTER:46344 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).

at 25400 ns(3): Note: BARREL SHIFTER:46337 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 25600 ns(1): Note: DATA REG:379607582 (/cordic\_tb/UUT/COS\_X\_REG/).

at 25600 ns(1): Note: DATA REG:379642542 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 25600 ns(1): Note: DATA REG:24723 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 25600 ns(1): Note: COUNTER:13 (/cordic\_tb/UUT/CORDIC\_COUNTER/).

at 25600 ns(3): Note: ROM:32768 (/cordic\_tb/UUT/LUT/).

at 25600 ns(3): Note: BARREL SHIFTER:23169 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).

at 25600 ns(3): Note: BARREL SHIFTER:23171 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 25800 ns(1): Note: DATA REG:379630753 (/cordic\_tb/UUT/COS\_X\_REG/).

at 25800 ns(1): Note: DATA REG:379619373 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 25800 ns(1): Note: DATA REG:8045 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 25800 ns(1): Note: COUNTER:14 (/cordic\_tb/UUT/CORDIC\_COUNTER/).

at 25800 ns(3): Note: ROM:16384 (/cordic\_tb/UUT/LUT/).

at 25800 ns(3): Note: BARREL SHIFTER:11585 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).

at 25800 ns(3): Note: BARREL SHIFTER:11585 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 26 us(1): Note: DATA REG:379619168 (/cordic\_tb/UUT/COS\_X\_REG/).

at 26 us(1): Note: DATA REG:379630958 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 26 us(1): Note: DATA REG:8339 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 26 us(1): Note: COUNTER:15 (/cordic\_tb/UUT/CORDIC\_COUNTER/).

at 26 us(3): Note: ROM:8192 (/cordic\_tb/UUT/LUT/).

at 26 us(3): Note: BARREL SHIFTER:5792 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).

at 26 us(3): Note: BARREL SHIFTER:5792 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 26200 ns(1): Note: DATA REG:379624960 (/cordic\_tb/UUT/COS\_X\_REG/).

at 26200 ns(1): Note: DATA REG:379625166 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 26200 ns(1): Note: DATA REG:147 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 26200 ns(1): Note: COUNTER:16 (/cordic\_tb/UUT/CORDIC\_COUNTER/).

at 26200 ns(3): Note: ROM:4096 (/cordic\_tb/UUT/LUT/).

at 26200 ns(3): Note: BARREL SHIFTER:2896 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).

at 26200 ns(3): Note: BARREL SHIFTER:2896 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 26400 ns(1): Note: DATA REG:379627856 (/cordic\_tb/UUT/COS\_X\_REG/).

at 26400 ns(1): Note: DATA REG:379622270 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 26400 ns(1): Note: DATA REG:3949 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 26400 ns(1): Note: COUNTER:17 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 26400 ns(3): Note: ROM:2048 (/cordic\_tb/UUT/LUT/).  
 at 26400 ns(3): Note: BARREL SHIFTER:1448 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 26400 ns(3): Note: BARREL SHIFTER:1448 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 26600 ns(1): Note: DATA REG:379626408 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 26600 ns(1): Note: DATA REG:379623718 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 26600 ns(1): Note: DATA REG:1901 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 26600 ns(1): Note: COUNTER:18 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 26600 ns(3): Note: ROM:1024 (/cordic\_tb/UUT/LUT/).  
 at 26600 ns(3): Note: BARREL SHIFTER:724 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 26600 ns(3): Note: BARREL SHIFTER:724 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 26800 ns(1): Note: DATA REG:379625684 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 26800 ns(1): Note: DATA REG:379624442 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 26800 ns(1): Note: DATA REG:877 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 26800 ns(1): Note: COUNTER:19 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 26800 ns(3): Note: ROM:512 (/cordic\_tb/UUT/LUT/).  
 at 26800 ns(3): Note: BARREL SHIFTER:362 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 26800 ns(3): Note: BARREL SHIFTER:362 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 27 us(1): Note: DATA REG:379625322 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 27 us(1): Note: DATA REG:379624804 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 27 us(1): Note: DATA REG:365 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 27 us(1): Note: COUNTER:20 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 27 us(3): Note: ROM:256 (/cordic\_tb/UUT/LUT/).  
 at 27 us(3): Note: BARREL SHIFTER:181 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 27 us(3): Note: BARREL SHIFTER:181 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 27200 ns(1): Note: DATA REG:379625141 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 27200 ns(1): Note: DATA REG:379624985 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 27200 ns(1): Note: DATA REG:109 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 27200 ns(1): Note: COUNTER:21 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 27200 ns(3): Note: ROM:128 (/cordic\_tb/UUT/LUT/).  
 at 27200 ns(3): Note: BARREL SHIFTER:90 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 27200 ns(3): Note: BARREL SHIFTER:90 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 27400 ns(1): Note: DATA REG:379625051 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 27400 ns(1): Note: DATA REG:379625075 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 27400 ns(1): Note: DATA REG:19 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 27400 ns(1): Note: COUNTER:22 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 27400 ns(3): Note: ROM:64 (/cordic\_tb/UUT/LUT/).  
 at 27400 ns(3): Note: BARREL SHIFTER:45 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 27400 ns(3): Note: BARREL SHIFTER:45 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 27600 ns(1): Note: DATA REG:379625096 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 27600 ns(1): Note: DATA REG:379625030 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 27600 ns(1): Note: DATA REG:45 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 27600 ns(1): Note: COUNTER:23 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 27600 ns(3): Note: ROM:32 (/cordic\_tb/UUT/LUT/).  
 at 27600 ns(3): Note: BARREL SHIFTER:22 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 27600 ns(3): Note: BARREL SHIFTER:22 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 27800 ns(1): Note: DATA REG:379625074 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 27800 ns(1): Note: DATA REG:379625052 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 27800 ns(1): Note: DATA REG:13 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 27800 ns(1): Note: COUNTER:24 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 27800 ns(3): Note: ROM:16 (/cordic\_tb/UUT/LUT/).  
 at 27800 ns(3): Note: BARREL SHIFTER:11 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 27800 ns(3): Note: BARREL SHIFTER:11 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 28 us(1): Note: DATA REG:379625063 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 28 us(1): Note: DATA REG:379625063 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 28 us(1): Note: DATA REG:3 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 28 us(1): Note: COUNTER:25 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 28 us(3): Note: ROM:8 (/cordic\_tb/UUT/LUT/).

at 28 us(3): Note: BARREL SHIFTER:5 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 28 us(3): Note: BARREL SHIFTER:5 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 28200 ns(1): Note: DATA REG:379625068 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 28200 ns(1): Note: DATA REG:379625058 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 28200 ns(1): Note: DATA REG:5 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 28200 ns(1): Note: COUNTER:26 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 28200 ns(3): Note: ROM:4 (/cordic\_tb/UUT/LUT/).  
at 28200 ns(3): Note: BARREL SHIFTER:2 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 28200 ns(3): Note: BARREL SHIFTER:2 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 28400 ns(1): Note: DATA REG:379625066 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 28400 ns(1): Note: DATA REG:379625060 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 28400 ns(1): Note: DATA REG:1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 28400 ns(1): Note: COUNTER:27 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 28400 ns(3): Note: ROM:2 (/cordic\_tb/UUT/LUT/).  
at 28400 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 28400 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 28600 ns(1): Note: DATA REG:379625065 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 28600 ns(1): Note: DATA REG:379625061 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 28600 ns(1): Note: DATA REG:-1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 28600 ns(1): Note: COUNTER:28 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 28600 ns(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
at 28600 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 28600 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 28800 ns(1): Note: DATA REG:379625065 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 28800 ns(1): Note: DATA REG:379625061 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 28800 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 28800 ns(1): Note: COUNTER:29 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 28800 ns(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
at 28800 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 28800 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 29 us(1): Note: DATA REG:379625065 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 29 us(1): Note: DATA REG:379625061 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 29 us(1): Note: DATA REG:-1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 29 us(1): Note: COUNTER:30 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 29 us(3): Note: ROM:0 (/cordic\_tb/UUT/LUT/).  
at 29 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 29 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 29 us(3): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
at 29200 ns(1): Note: DATA REG:379625065 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 29200 ns(1): Note: DATA REG:379625061 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 29200 ns(1): Note: DATA REG:-1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 29200 ns(1): Note: COUNTER:31 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 29200 ns(2): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).  
at 29200 ns(3): Note: ROM:421657428 (/cordic\_tb/UUT/LUT/).  
at 29200 ns(3): Note: BARREL SHIFTER:379625065 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 29200 ns(3): Note: BARREL SHIFTER:379625061 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 29200 ns(3): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).  
at 30 us(2): Note: FSM:IDLE (/cordic\_tb/UUT/FSM/).  
at 30200 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 30200 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 30200 ns(1): Note: DATA REG:281104952 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 30200 ns(2): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
at 30400 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 30400 ns(1): Note: DATA REG:326016437 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 30400 ns(1): Note: DATA REG:-140552476 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 30400 ns(1): Note: COUNTER:0 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 30400 ns(3): Note: ROM:248918915 (/cordic\_tb/UUT/LUT/).  
at 30400 ns(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 30400 ns(3): Note: BARREL SHIFTER:163008218 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).

at 30600 ns(1): Note: DATA REG:489024655 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 30600 ns(1): Note: DATA REG:163008219 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 30600 ns(1): Note: DATA REG:108366439 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 30600 ns(1): Note: COUNTER:1 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 30600 ns(3): Note: ROM:131521918 (/cordic\_tb/UUT/LUT/).  
at 30600 ns(3): Note: BARREL SHIFTER:122256163 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 30600 ns(3): Note: BARREL SHIFTER:40752054 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 30800 ns(1): Note: DATA REG:448272601 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 30800 ns(1): Note: DATA REG:285264382 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 30800 ns(1): Note: DATA REG:-23155479 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 30800 ns(1): Note: COUNTER:2 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 30800 ns(3): Note: ROM:66762579 (/cordic\_tb/UUT/LUT/).  
at 30800 ns(3): Note: BARREL SHIFTER:56034075 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 30800 ns(3): Note: BARREL SHIFTER:35658047 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 31 us(1): Note: DATA REG:483930648 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 31 us(1): Note: DATA REG:229230307 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 31 us(1): Note: DATA REG:43607100 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 31 us(1): Note: COUNTER:3 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 31 us(3): Note: ROM:33510843 (/cordic\_tb/UUT/LUT/).  
at 31 us(3): Note: BARREL SHIFTER:30245665 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 31 us(3): Note: BARREL SHIFTER:14326894 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 31200 ns(1): Note: DATA REG:469603754 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 31200 ns(1): Note: DATA REG:259475972 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 31200 ns(1): Note: DATA REG:10096257 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 31200 ns(1): Note: COUNTER:4 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 31200 ns(3): Note: ROM:16771758 (/cordic\_tb/UUT/LUT/).  
at 31200 ns(3): Note: BARREL SHIFTER:14675117 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 31200 ns(3): Note: BARREL SHIFTER:8108624 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 31400 ns(1): Note: DATA REG:461495130 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 31400 ns(1): Note: DATA REG:274151089 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 31400 ns(1): Note: DATA REG:-6675501 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 31400 ns(1): Note: COUNTER:5 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 31400 ns(3): Note: ROM:8387925 (/cordic\_tb/UUT/LUT/).  
at 31400 ns(3): Note: BARREL SHIFTER:7210861 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 31400 ns(3): Note: BARREL SHIFTER:4283610 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 31600 ns(1): Note: DATA REG:465778740 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 31600 ns(1): Note: DATA REG:266940228 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 31600 ns(1): Note: DATA REG:1712424 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 31600 ns(1): Note: COUNTER:6 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 31600 ns(3): Note: ROM:4194219 (/cordic\_tb/UUT/LUT/).  
at 31600 ns(3): Note: BARREL SHIFTER:3638896 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 31600 ns(3): Note: BARREL SHIFTER:2085470 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 31800 ns(1): Note: DATA REG:463693270 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 31800 ns(1): Note: DATA REG:270579124 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 31800 ns(1): Note: DATA REG:-2481795 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 31800 ns(1): Note: COUNTER:7 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 31800 ns(3): Note: ROM:2097141 (/cordic\_tb/UUT/LUT/).  
at 31800 ns(3): Note: BARREL SHIFTER:1811301 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 31800 ns(3): Note: BARREL SHIFTER:1056949 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 32 us(1): Note: DATA REG:464750219 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 32 us(1): Note: DATA REG:268767823 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 32 us(1): Note: DATA REG:-384654 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
at 32 us(1): Note: COUNTER:8 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
at 32 us(3): Note: ROM:1048575 (/cordic\_tb/UUT/LUT/).  
at 32 us(3): Note: BARREL SHIFTER:907715 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
at 32 us(3): Note: BARREL SHIFTER:524937 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
at 32200 ns(1): Note: DATA REG:465275156 (/cordic\_tb/UUT/COS\_X\_REG/).  
at 32200 ns(1): Note: DATA REG:267860108 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
at 32200 ns(1): Note: DATA REG:663921 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).

at 32200 ns(1): Note: COUNTER:9 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 32200 ns(3): Note: ROM:524288 (/cordic\_tb/UUT/LUT/).  
 at 32200 ns(3): Note: BARREL SHIFTER:454370 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 32200 ns(3): Note: BARREL SHIFTER:261582 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 32400 ns(1): Note: DATA REG:465013574 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 32400 ns(1): Note: DATA REG:268314478 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 32400 ns(1): Note: DATA REG:139633 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 32400 ns(1): Note: COUNTER:10 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 32400 ns(3): Note: ROM:262144 (/cordic\_tb/UUT/LUT/).  
 at 32400 ns(3): Note: BARREL SHIFTER:227057 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 32400 ns(3): Note: BARREL SHIFTER:131012 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 32600 ns(1): Note: DATA REG:464882562 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 32600 ns(1): Note: DATA REG:268541535 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 32600 ns(1): Note: DATA REG:-122511 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 32600 ns(1): Note: COUNTER:11 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 32600 ns(3): Note: ROM:131072 (/cordic\_tb/UUT/LUT/).  
 at 32600 ns(3): Note: BARREL SHIFTER:113496 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 32600 ns(3): Note: BARREL SHIFTER:65561 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 32800 ns(1): Note: DATA REG:464948123 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 32800 ns(1): Note: DATA REG:268428039 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 32800 ns(1): Note: DATA REG:8561 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 32800 ns(1): Note: COUNTER:12 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 32800 ns(3): Note: ROM:65536 (/cordic\_tb/UUT/LUT/).  
 at 32800 ns(3): Note: BARREL SHIFTER:56756 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 32800 ns(3): Note: BARREL SHIFTER:32767 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 33 us(1): Note: DATA REG:464915356 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 33 us(1): Note: DATA REG:268484795 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 33 us(1): Note: DATA REG:-56975 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 33 us(1): Note: COUNTER:13 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 33 us(3): Note: ROM:32768 (/cordic\_tb/UUT/LUT/).  
 at 33 us(3): Note: BARREL SHIFTER:28376 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 33 us(3): Note: BARREL SHIFTER:16387 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 33200 ns(1): Note: DATA REG:464931743 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 33200 ns(1): Note: DATA REG:268456419 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 33200 ns(1): Note: DATA REG:-24207 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 33200 ns(1): Note: COUNTER:14 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 33200 ns(3): Note: ROM:16384 (/cordic\_tb/UUT/LUT/).  
 at 33200 ns(3): Note: BARREL SHIFTER:14188 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 33200 ns(3): Note: BARREL SHIFTER:8192 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 33400 ns(1): Note: DATA REG:464939935 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 33400 ns(1): Note: DATA REG:268442231 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 33400 ns(1): Note: DATA REG:-7823 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 33400 ns(1): Note: COUNTER:15 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 33400 ns(3): Note: ROM:8192 (/cordic\_tb/UUT/LUT/).  
 at 33400 ns(3): Note: BARREL SHIFTER:7094 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 33400 ns(3): Note: BARREL SHIFTER:4096 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 33600 ns(1): Note: DATA REG:464944031 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 33600 ns(1): Note: DATA REG:268435137 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 33600 ns(1): Note: DATA REG:369 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 33600 ns(1): Note: COUNTER:16 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 33600 ns(3): Note: ROM:4096 (/cordic\_tb/UUT/LUT/).  
 at 33600 ns(3): Note: BARREL SHIFTER:3547 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 33600 ns(3): Note: BARREL SHIFTER:2047 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 33800 ns(1): Note: DATA REG:464941984 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 33800 ns(1): Note: DATA REG:268438684 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 33800 ns(1): Note: DATA REG:-3727 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 33800 ns(1): Note: COUNTER:17 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 33800 ns(3): Note: ROM:2048 (/cordic\_tb/UUT/LUT/).  
 at 33800 ns(3): Note: BARREL SHIFTER:1773 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).



at 33800 ns(3): Note: BARREL SHIFTER:1024 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 34 us(1): Note: DATA REG:464943008 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 34 us(1): Note: DATA REG:268436911 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 34 us(1): Note: DATA REG:-1679 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 34 us(1): Note: COUNTER:18 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 34 us(3): Note: ROM:1024 (/cordic\_tb/UUT/LUT/).  
 at 34 us(3): Note: BARREL SHIFTER:886 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 34 us(3): Note: BARREL SHIFTER:512 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 34200 ns(1): Note: DATA REG:464943520 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 34200 ns(1): Note: DATA REG:268436025 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 34200 ns(1): Note: DATA REG:-655 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 34200 ns(1): Note: COUNTER:19 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 34200 ns(3): Note: ROM:512 (/cordic\_tb/UUT/LUT/).  
 at 34200 ns(3): Note: BARREL SHIFTER:443 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 34200 ns(3): Note: BARREL SHIFTER:256 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 34400 ns(1): Note: DATA REG:464943776 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 34400 ns(1): Note: DATA REG:268435582 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 34400 ns(1): Note: DATA REG:-143 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 34400 ns(1): Note: COUNTER:20 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 34400 ns(3): Note: ROM:256 (/cordic\_tb/UUT/LUT/).  
 at 34400 ns(3): Note: BARREL SHIFTER:221 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 34400 ns(3): Note: BARREL SHIFTER:128 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 34600 ns(1): Note: DATA REG:464943904 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 34600 ns(1): Note: DATA REG:268435361 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 34600 ns(1): Note: DATA REG:113 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 34600 ns(1): Note: COUNTER:21 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 34600 ns(3): Note: ROM:128 (/cordic\_tb/UUT/LUT/).  
 at 34600 ns(3): Note: BARREL SHIFTER:110 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 34600 ns(3): Note: BARREL SHIFTER:63 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 34800 ns(1): Note: DATA REG:464943841 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 34800 ns(1): Note: DATA REG:268435471 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 34800 ns(1): Note: DATA REG:-15 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 34800 ns(1): Note: COUNTER:22 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 34800 ns(3): Note: ROM:64 (/cordic\_tb/UUT/LUT/).  
 at 34800 ns(3): Note: BARREL SHIFTER:55 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 34800 ns(3): Note: BARREL SHIFTER:32 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 35 us(1): Note: DATA REG:464943873 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 35 us(1): Note: DATA REG:268435416 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 35 us(1): Note: DATA REG:49 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 35 us(1): Note: COUNTER:23 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 35 us(3): Note: ROM:32 (/cordic\_tb/UUT/LUT/).  
 at 35 us(3): Note: BARREL SHIFTER:27 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 35 us(3): Note: BARREL SHIFTER:15 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 35200 ns(1): Note: DATA REG:464943858 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 35200 ns(1): Note: DATA REG:268435443 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 35200 ns(1): Note: DATA REG:17 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 35200 ns(1): Note: COUNTER:24 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 35200 ns(3): Note: ROM:16 (/cordic\_tb/UUT/LUT/).  
 at 35200 ns(3): Note: BARREL SHIFTER:13 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 35200 ns(3): Note: BARREL SHIFTER:7 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 35400 ns(1): Note: DATA REG:464943851 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 35400 ns(1): Note: DATA REG:268435456 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 35400 ns(1): Note: DATA REG:1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 35400 ns(1): Note: COUNTER:25 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 35400 ns(3): Note: ROM:8 (/cordic\_tb/UUT/LUT/).  
 at 35400 ns(3): Note: BARREL SHIFTER:6 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 35400 ns(3): Note: BARREL SHIFTER:4 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 35600 ns(1): Note: DATA REG:464943847 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 35600 ns(1): Note: DATA REG:268435462 (/cordic\_tb/UUT/SIN\_Y\_REG/).

at 35600 ns(1): Note: DATA REG:7 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 35600 ns(1): Note: COUNTER:26 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 35600 ns(3): Note: ROM:4 (/cordic\_tb/UUT/LUT/).  
 at 35600 ns(3): Note: BARREL SHIFTER:3 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 35600 ns(3): Note: BARREL SHIFTER:2 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 35800 ns(1): Note: DATA REG:464943849 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 35800 ns(1): Note: DATA REG:268435459 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 35800 ns(1): Note: DATA REG:3 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 35800 ns(1): Note: COUNTER:27 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 35800 ns(3): Note: ROM:2 (/cordic\_tb/UUT/LUT/).  
 at 35800 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 35800 ns(3): Note: BARREL SHIFTER:1 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 36 us(1): Note: DATA REG:464943850 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 36 us(1): Note: DATA REG:268435458 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 36 us(1): Note: DATA REG:1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 36 us(1): Note: COUNTER:28 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 36 us(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
 at 36 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 36 us(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 36200 ns(1): Note: DATA REG:464943850 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 36200 ns(1): Note: DATA REG:268435458 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 36200 ns(1): Note: DATA REG:0 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 36200 ns(1): Note: COUNTER:29 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 36200 ns(3): Note: ROM:1 (/cordic\_tb/UUT/LUT/).  
 at 36200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 36200 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 36400 ns(1): Note: DATA REG:464943850 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 36400 ns(1): Note: DATA REG:268435458 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 36400 ns(1): Note: DATA REG:1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 36400 ns(1): Note: COUNTER:30 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 36400 ns(3): Note: ROM:0 (/cordic\_tb/UUT/LUT/).  
 at 36400 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 36400 ns(3): Note: BARREL SHIFTER:0 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 36400 ns(3): Note: FSM:CALC (/cordic\_tb/UUT/FSM/).  
 at 36600 ns(1): Note: DATA REG:464943850 (/cordic\_tb/UUT/COS\_X\_REG/).  
 at 36600 ns(1): Note: DATA REG:268435458 (/cordic\_tb/UUT/SIN\_Y\_REG/).  
 at 36600 ns(1): Note: DATA REG:1 (/cordic\_tb/UUT/ANGLE\_Z\_REG/).  
 at 36600 ns(1): Note: COUNTER:31 (/cordic\_tb/UUT/CORDIC\_COUNTER/).  
 at 36600 ns(2): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).  
 at 36600 ns(3): Note: ROM:421657428 (/cordic\_tb/UUT/LUT/).  
 at 36600 ns(3): Note: BARREL SHIFTER:464943850 (/cordic\_tb/UUT/BARREL\_SHIFT\_X/).  
 at 36600 ns(3): Note: BARREL SHIFTER:268435458 (/cordic\_tb/UUT/BARREL\_SHIFT\_Y/).  
 at 36600 ns(3): Note: FSM:FINISHED (/cordic\_tb/UUT/FSM/).

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